

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***414  
700  
5-2-03*

Applicant(s): M. FUNABASHI



Serial No.: 09/902,673

Filed: July 12, 2001

For: METHOD OF MANUFACTURING A SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE

Group: 2825

Examiner: L. Malsawma

**PETITION FOR EXTENSION OF TIME**Assistant Commissioner for Patents  
Washington, D.C. 20231

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April 29, 2003

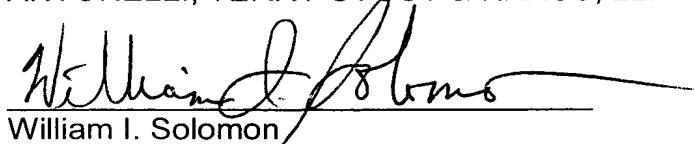
Sir:

In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing a response within the first month subsequent to expiration of the shortened statutory period set by filing the Notice of Appeal filed January 29, 2003. A Credit Card Payment Form in the amount of \$110.00 to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135  
(Case No. 843.37558VX1).

Respectfully submitted,

ANTONELLI, TERRY STOUT &amp; KRAUS, LLP



William I. Solomon  
Registration No. 28,565

WIS/sjg

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